

Express Mail Label No. EL844519025US
Attorney's Docket No.: S1022/8717

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: S1022/8717
Serial No.: Unassigned
Filed: Herewith
For: ARITHMETIC UNIT

Examiner: Unassigned
Art Unit: Unassigned

Box Patent Application
Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir/Madam:

Prior to examination please amend the above-identified application as follows:

IN THE SPECIFICATION

Please insert the following paragraphs on page 18, after line 21.

--Having thus described at least one illustrative embodiment of the invention, various alterations, modifications, and improvements will readily occur to those skilled in the art. Such alterations, modifications, and improvements are intended to be within the spirit and scope of the invention. Accordingly, the foregoing description is by way of example only and is not intended as limiting. The invention is limited only as defined in the following claims and the equivalents thereto.

What is claimed is:--

IN THE ABSTRACT

Please amend the abstract as follows.

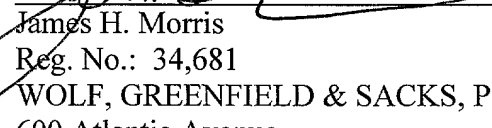
Applicant presents the abstract as amended below and encloses a separate sheet indicating the amendments to the abstract with bracketing and underlining.

An arithmetic unit for multiplying a first quantity x by a second quantity y, the arithmetic unit including a Booth coder having a plurality of inputs for receiving a plurality bits of the second quantity and a plurality of outputs for providing Booth coded outputs; and circuitry connected to at least one of the inputs and the outputs for modifying at least one output of the coder.

REMARKS

This is a Preliminary Amendment in which Applicant has amended the abstract and the specification. No new matter has been added. An early and favorable action is respectfully requested.

Respectfully submitted,



James H. Morris
Reg. No.: 34,681
WOLF, GREENFIELD & SACKS, P.C.
600 Atlantic Avenue
Boston, Massachusetts 02210
Tel. (617) 720-3500

AMENDED ABSTRACT SHOWING THE AMENDMENTS

5 An arithmetic unit for multiplying a first quantity x by a second quantity y, [said] the
arithmetic unit [comprising] including a Booth coder having a plurality of inputs for
receiving a plurality bits of the second quantity and a plurality of outputs for providing
Booth coded outputs; and circuitry [means] connected to at least one of [said] the inputs and
[said] the outputs for modifying at least one output of the coder.

09496-073001